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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This appln claims benefit of 60/400,391 07/31/2002

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

EUROPEAN PATENT OFFICE (EPO) 03291917.7 07/30/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/27/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY FRANCE	SHEETS DRAWING 5	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged  Examiner's Signature: <i>Jacob Petrucci</i> Initials: <i>P</i>				

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## TITLE

Processor that accommodates multiple instruction sets and multiple decode modes

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